

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,495,672 B2
APPLICATION NO. : 10/539813
DATED : February 24, 2009
INVENTOR(S) : Akenine-Moller et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 1, Line 54, delete “GeForce3graphics” and insert -- GeForce3 graphics --, therefor.

In Column 2, Line 46, after “invention” insert -- , --.

In Column 2, Line 49, after “invention,” delete “and”.

In Column 2, Line 52, delete “scheme.” and insert -- scheme, and --, therefor.

In Column 3, Line 25, delete “memory-207” and insert -- memory 207 --, therefor.

In Column 3, Line 37, delete “individual-pixels” and insert -- individual pixels --, therefor.

In Column 4, Line 22, delete “mirroring-the” and insert -- mirroring the --, therefor.

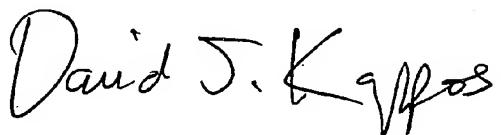
In Column 4, Lines 50-51, delete “contains-four” and insert -- contains four --, therefor.

In Column 5, Line 46, delete “or-the” and insert -- or the --, therefor.

In Column 6, Line 32, delete “the-triangle.” and insert -- the triangle. --, therefor.

Signed and Sealed this

Twenty-seventh Day of October, 2009



David J. Kappos
Director of the United States Patent and Trademark Office